

PAT-NO: JP401311792A
DOCUMENT- IDENTIFIER: JP 01311792 A
TITLE: REMOTE MONITORING CONTROL SYSTEM
PUBN-DATE: December 15, 1989

INVENTOR- INFORMATION:

NAME
FUKUDA, SHUICHI

ASSIGNEE- INFORMATION:

NAME NEC CORP	COUNTRY N/A
------------------	----------------

APPL-NO: JP63143063

APPL-DATE: June 10, 1988

INT-CL (IPC): H04Q009/16

US-CL-CURRENT: 375/310

ABSTRACT:

PURPOSE: To separately reset/restart the CPU of a controlled station from a control station by providing a proper continuous signal generating circuit at the respective controlled station in the control station and providing a restarting signal generating circuit in the respective controlled stations.

CONSTITUTION: When a controlled station 11b is stopped or required to be reset, a control station 9 drives a continuous signal generator 10 and transmits a proper data '1' continuous signal fixed in advance only twice to the station 11b equivalent to a data error through a down transmitting data line 5, a transmitting device 2a and a communication circuit 3. The station

11b outputs a reset/restart signal to an CPU by that it is detected by a restart signal generating circuit 12b that the received signal is the data '1' and it is transmitted twice. The CPU restarts with receiving the reset/restart signal.

COPYRIGHT: (C)1989,JPO&Japio